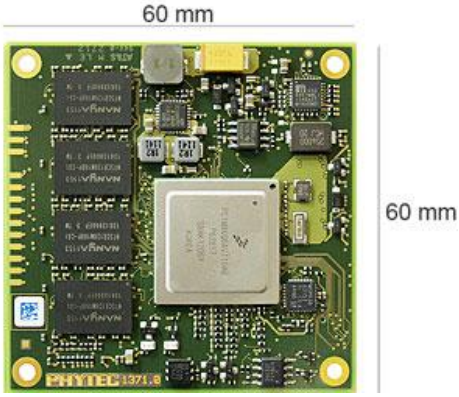
# phyCARD-XL3-i.MX6

The phyCARD-XL3-i.MX6 SOM is populated with the Freescale i.MX6 providing scalability of computing performance through offered single, dual, and quad core variants. Additionally, due to identical break-out of interface signals per PHYTEC's innovative X-Arc embedded bus standard, the phyCARD-XL3-i.MX6 is compatible with other phyCARD family members making resources available at a single, 100-pin connector including Ethernet, USB, UART, SPI, I2C, audio, SDIO/MMC, GPIO, JTAG, display (LVDS), and more.

## phyCARD-XL3-i.MX6 System on Module

**SOM Highlights**

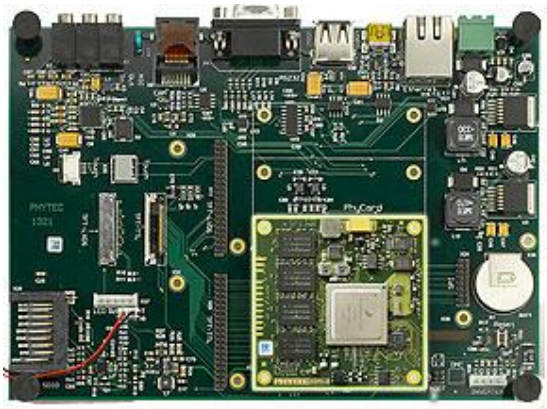
- i.MX6 Single, Dual, Quad core
- Up to 1 GHz/core
- VPU, IPUv3H, GPU2Dv2, OpenVG 1.2
- ARM® Cortex™-A9
- Up to 1 GB DDR3 / 8 GB NAND
- X-Arc phyCARD standard Bus
- Linux and Compact 7 BSPs




## phyCARD-XL3-i.MX6 Rapid Development Kit

**Kit Contents**

- phyCARD-XL3-i.MX6 System on Module
- phyBASE Carrier Board
- Linux or Compact 7 Software
- Optional LCD
- Cables, schematics, and kit CD



**Carrier Board Features**

- PHYTEC X-Arc embedded bus standard
- RJ-45 Ethernet
- USB 2.0 OTG port
- 3x USB 2.0 Host interfaces
- RS-232 serial port
- SD/MMC card slot
- Stereo input and output jacks
- 3x user programmable GPIOs
- LVDS camera interface
- Universal display interface
- Lattice LC4256V PLD